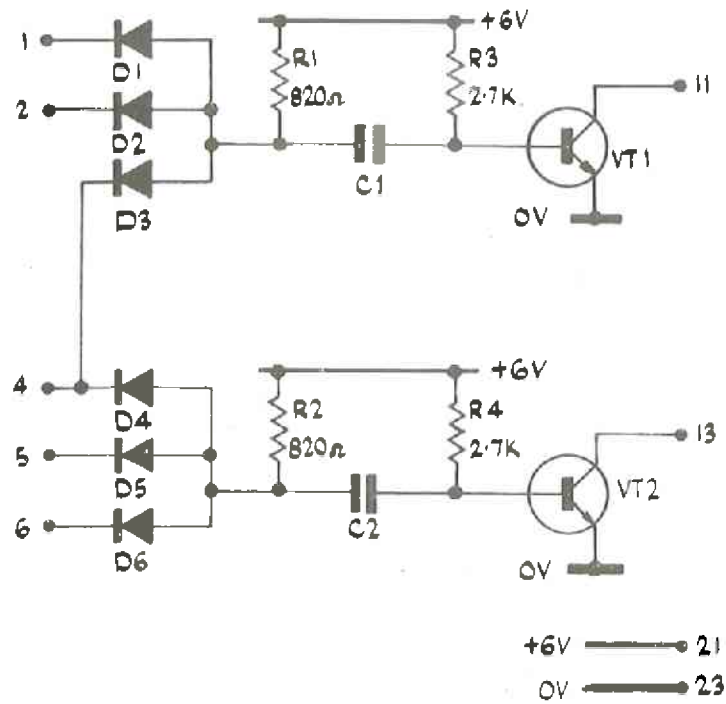


PULSE WIDTH ( $\Delta$ ) =  $C_{ns}$  (WHERE  $C$  = CAPACITANCE IN pF.)

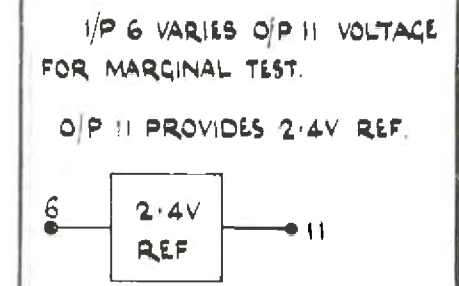
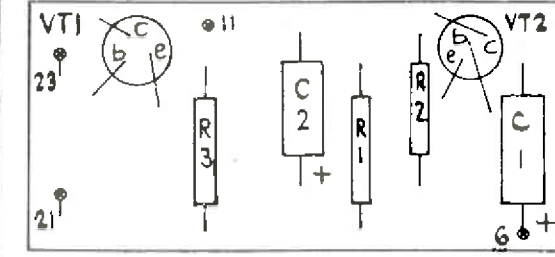
DIODES ARE PURCH 101  
TRANSISTORS ARE PURCH 100



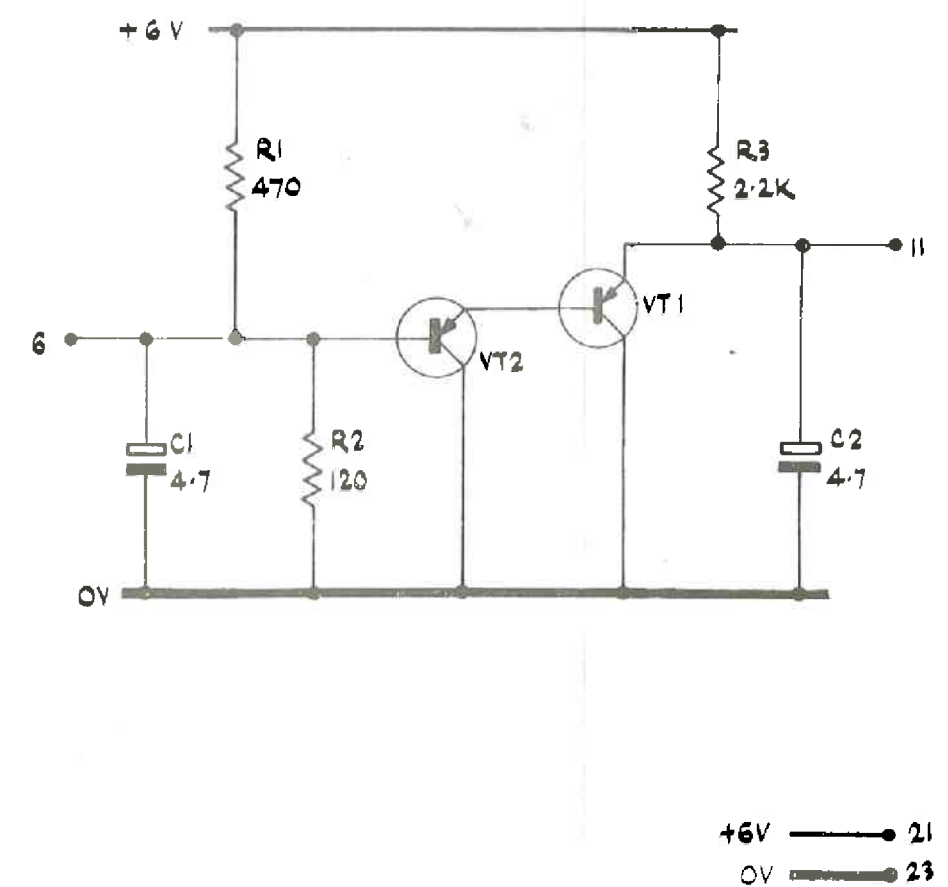
NOTE:-  
PULSE WIDTH (+VE). PULSE TRIGGERED BY ANY INPUT REVERTING TO '0' PROVIDING ALL I/P'S HAVE BEEN '1' FOR GREATER THAN  $N/2$  SEC.  $1/P$  4. — 2.4V REFERENCE VOLTAGE.

LSA. No.	C <sub>1</sub>	C <sub>2</sub>
07	100	100
13	330	330
34	100	330

DRAWN	C.A.C.	ISSUE No.	1	2	3
CHECKED	CS 456	A.R. No.	1374	1505	1997
APPROVED	[Signature]	DATE	26-4-66 29-6-66 20-4-67		
DATE	16/5/66	INITIALS	C.A.C. R.W.C. SJS		



1/P 6 VARIES O/P 11 VOLTAGE FOR MARGINAL TEST.  
O/P 11 PROVIDES 2.4V REF.



+6V ——— 21  
0V ——— 23

DRAWN	C.A.C.	ISSUE No.	1	2	3
CHECKED	CS 456	A.R. No.	1374	1505	1997
APPROVED	[Signature]	DATE	26-4-66 29-6-66 20-4-67		
DATE	16/5/66	INITIALS	C.A.C. R.W.C. SJS		