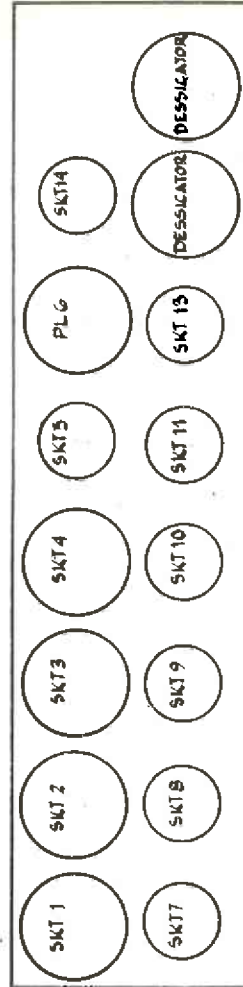


- SKT. 1 DISPLAY UNIT PL1.
- SKT. 2 DISPLAY UNIT PL2.
- SKT. 3 DISPLAY UNIT PL3.
- SKT. 4 INPUT OUTPUT UNIT (DATA)
- SKT. 5 CONTROL UNIT PL1
- SKT. 7 MARGINAL TEST UNIT PL1
- SKT. 8 ADDITIONAL STORE
- SKT. 9 ADDITIONAL STORE
- SKT. 10 PAPER TAPE CONTROLLER PL2
- SKT. 11 CONTROL UNIT PL2.
- PL. 6. COMPUTER POWER SUPPLY SKT. 2.
- SKT. 13. INPUT OUTPUT UNIT (CONTROL)
- SKT. 14. EXTRA STORE



1	A - FT CONTROL KEY INPUT	32	SPARE	32	A - GZ TEMP SENSING & CONTROL	32
2	A - FL MANUAL CONTROL LOGIC	32	A - FB WAVEFORM AMPLIFIERS	32	A - E A3 INHIBIT DRIVER & SENSE AMP	40
3	A - FD PROGRAM INTERRUPT	32	A - GL INITIAL INSTRUCTIONS 2	32	A - E A3 INHIBIT DRIVER & SENSE AMP	40
4	A - FC TIMING	32	A - GK INITIAL INSTRUCTIONS 1	32	A - E A3 INHIBIT DRIVER & SENSE AMP	40
5	A - FR TIMING WAVEFORM DRIVERS	32	A - FA REGISTERS (BIT 18)	32	A - E A3 INHIBIT DRIVER & SENSE AMP	40
6	A - FI CONTROL FUNCTION DRIVE	32	A - FA REGISTERS (BIT 17)	32	A - E A3 INHIBIT DRIVER & SENSE AMP	40
7	A - FH CONTROL MATRIX ^{CONDITIONAL} A, B, C	32	A - FA REGISTERS (BIT 16)	32	A - E A3 INHIBIT DRIVER & SENSE AMP	40
8	A - FB CONTROL MATRIX AMPLIFIERS 1	32	A - FA REGISTERS (BIT 15)	32	A - E A3 INHIBIT DRIVER & SENSE AMP	40
9	A - FB CONTROL MATRIX AMPLIFIERS 2	32	A - FA REGISTERS (BIT 14)	32	A - E A3 INHIBIT DRIVER & SENSE AMP	40
10	A - GJ CONTROL MATRIX (FN 15)	32	A - FA REGISTERS (BIT 13)	32	A - E A3 INHIBIT DRIVER & SENSE AMP	40
11	A - GI CONTROL MATRIX (FN 14A)	32	A - FA REGISTERS (BIT 12)	32	A - E A3 INHIBIT DRIVER & SENSE AMP	40
12	A - GH CONTROL MATRIX (FN 14)	32	A - FA REGISTERS (BIT 11)	32		
13	A - GG CONTROL MATRIX (FN 13)	32	A - FA REGISTERS (BIT 10)	32	X1	READ OUT
14	A - GF CONTROL MATRIX (FN 12)	32	A - FK WAVEFORM DRIVERS 2	32	X2	
15	A - GE CONTROL MATRIX (FN 5, 7, 8)	32	A - FJ WAVEFORM DRIVERS 1	32	X3	Y1
16	A - GD CONTROL MATRIX (FN 56, 9)	32	A - FA REGISTERS (BIT 9)	32	X4	Y2
17	A - GC CONTROL MATRIX (FNS 2, 10, 11)	32	A - FA REGISTERS (BIT 8)	32	X5	Y3
18	A - GB CONTROL MATRIX (FNS 0, 13, 4)	32	A - FA REGISTERS (BIT 7)	32	X6	INHIBIT
19	A - GA MATRIX (CONTROL CHAIN)	32	A - FA REGISTERS (BIT 6)	32		
20	A - FG CONTROL MATRIX ADDRESS DRIVER	32	A - FA REGISTERS (BIT 5)	32		
21	A - FF CONTROL MATRIX ADDRESS DRIVER	32	A - FA REGISTERS (BIT 4)	32		
22	A - FE FUNCTION DECODE TEST	32	A - FA REGISTERS (BIT 3)	32		
23	A - FP PROCESS COUNTER (BD 1)	32	A - FA REGISTERS (BIT 2)	32		
24	A - FP PROCESS COUNTER (BD 2)	32	A - FA REGISTERS (BIT 1)	32		
25	A - FP PROCESS COUNTER (BD 3)	32	A - FM STORE CONTROL & OVERFLOW LOGIC	32		
26	* A - FV CABLE RECEIVERS 1	32	A - EB3 STORE CONTROL CHAIN UNIT	40		
27	* A - FJ CABLE TRANSMITTERS	32	A - EC3 MASTER DRIVERS	40	A - AE SELECTION DIODES	40
28	A - FO CABLE RECEIVERS 1	32	A - ED3 DRIVER & GATES	40	A - AE SELECTION DIODES	40
29	A - FS CABLE RECEIVERS 2	32	A - ED3 DRIVERS & GATES	40	A - AE SELECTION DIODES	40
30	A - FQ INPUT, OUTPUT, CONTROL	32	A - ED3 DRIVERS & GATES	40	A - AE SELECTION DIODES	40
31	A - FN CABLE TRANSMITTERS 1	32	A - ED3 DRIVERS & GATES	40	A - AE SELECTION DIODES	40
32	A - FN CABLE TRANSMITTERS 2	32	A - ED3 DRIVERS & GATES	40	A - AE SELECTION DIODES	40

VIEW ON BACK WIRING SIDE

* ONLY SUPPLIED WITH EXTRA STORE UNITS

MSC 920B COMPUTER
CONNECTOR AND UNIT LAYOUT

A3