

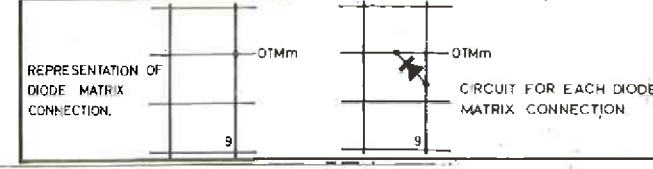
TRUTH TABLE FOR FN 1 ADDITION

INPUTS			OUTPUTS	
M _n	A _n	C _{n-1}	C _n	F _n
0	0	0	0	0
0	0	1	0	1
0	1	0	0	1
0	1	1	1	0
1	0	0	0	1
1	0	1	1	0
1	1	0	1	0
1	1	1	1	1

TRUTH TABLE FOR FN 2 SUBTRACTION

INPUTS			OUTPUTS	
M _n	A _n	C _{n-1}	C _n	F _n
0	0	0	0	1
0	0	1	1	0
0	1	0	0	0
0	1	1	0	1
1	0	0	1	0
1	0	1	1	1
1	1	0	0	1
1	1	1	1	0

NOTE
C_{n-1} TO BIT 1 IS MADE EQUAL TO 1 FOR FN 2



NOTE - ALL WAVEFORMS WITH THE SUFFIX 'm' HAVE A TRUE LEVEL OF APPROX. +0.7 VOLTS

TO BE READ IN CONJUNCTION WITH 322 A 7191

TITLE
920B COMPUTER
MATRICES FOR FUNCTIONS
0, 1, 2, 3, 6, 7, 8, 9, 10 & 11.
SHEET 1 OF 4 SHEETS

A20